

Title: MULTIPLEXER CIRCUIT FOR
CONVERTING PARALLEL DATA INTO SERIAL
DATA AT HIGH SPEED AND SYNCHRONIZING
THE SERIAL DATA WITH A CLOCK SIGNAL
Inventor: Shunichiro MASAKI
App. No.: Unknown Docket No.: 100021-00134

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Fig.1
(PRIOR ART)

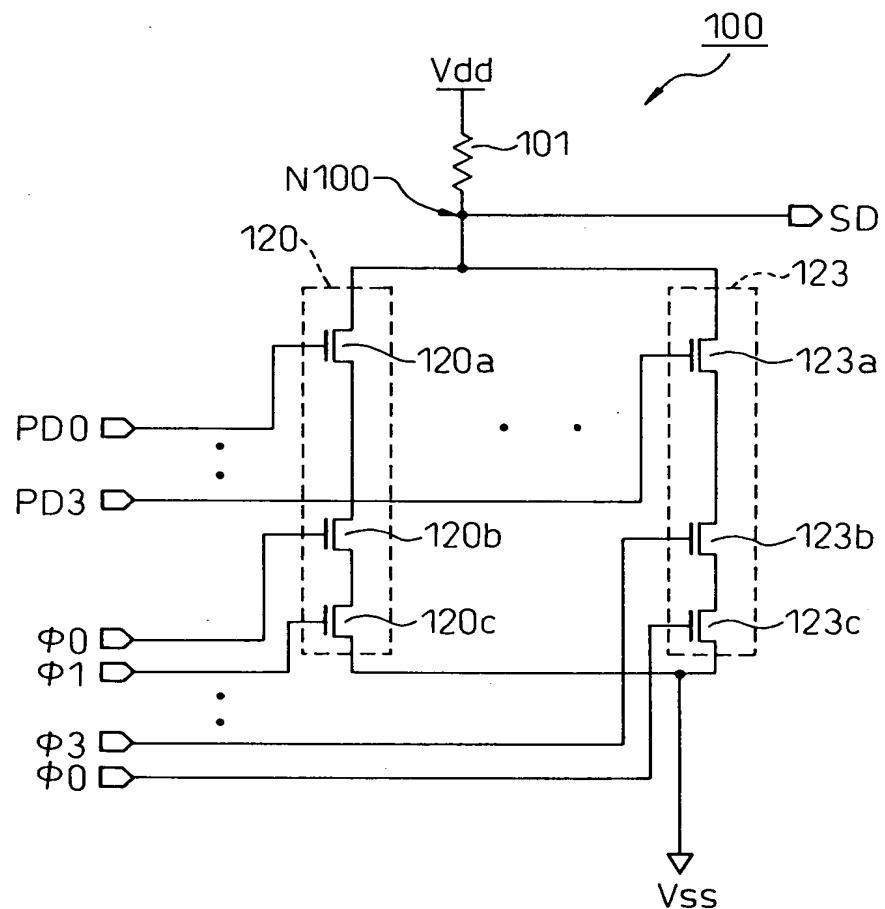


Fig.2
(PRIOR ART)

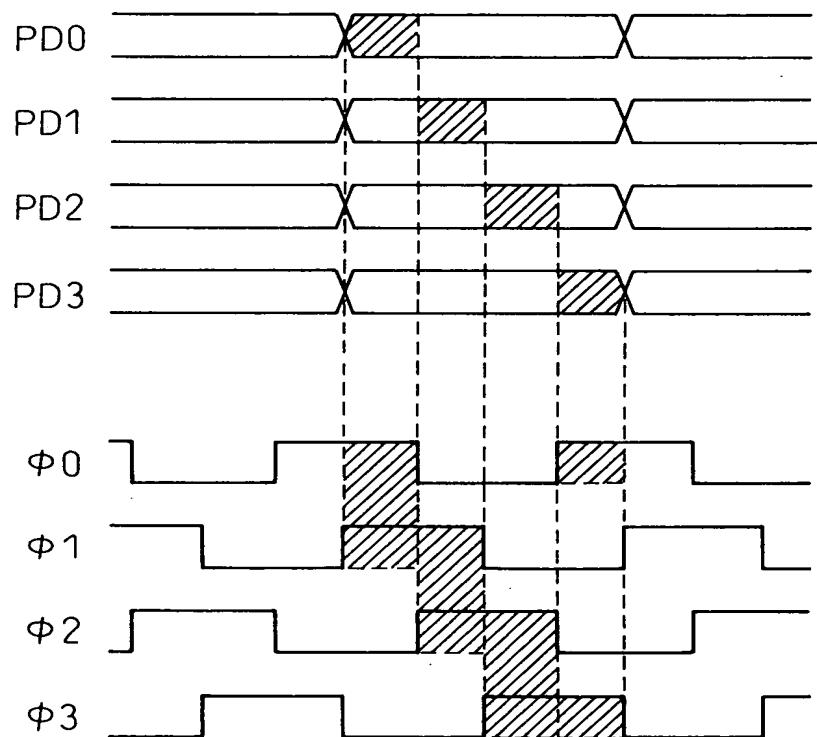


Fig.3

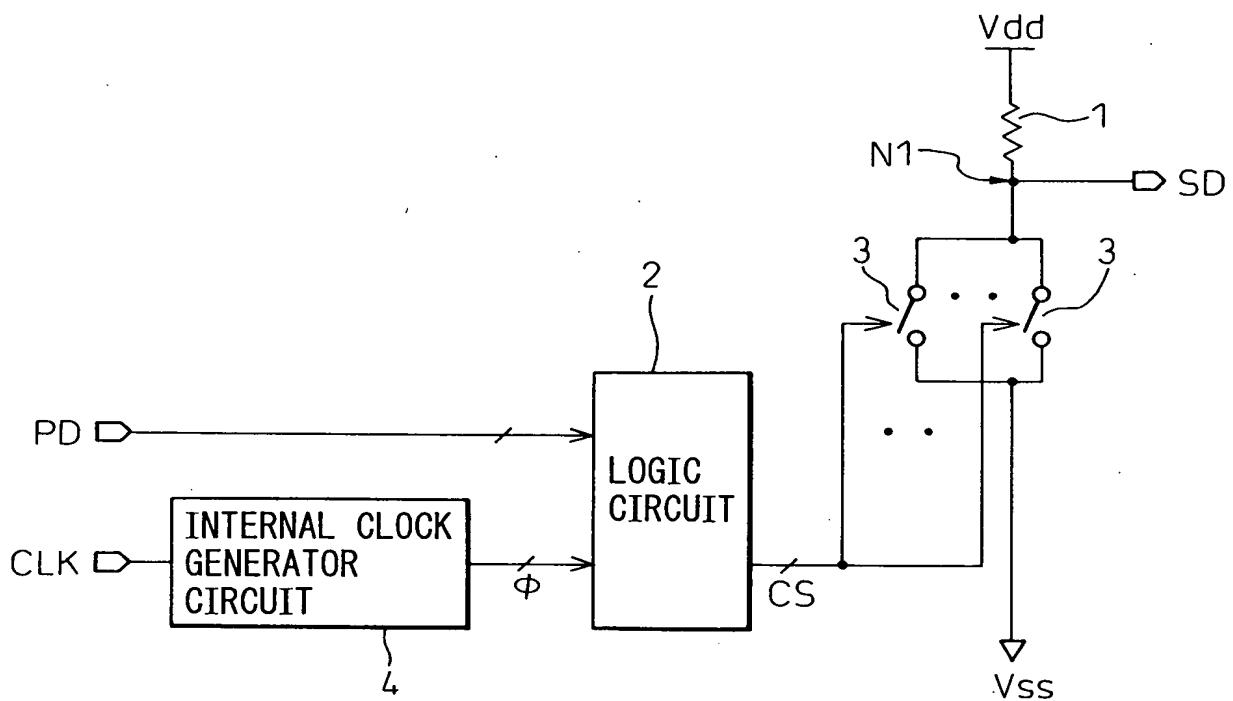
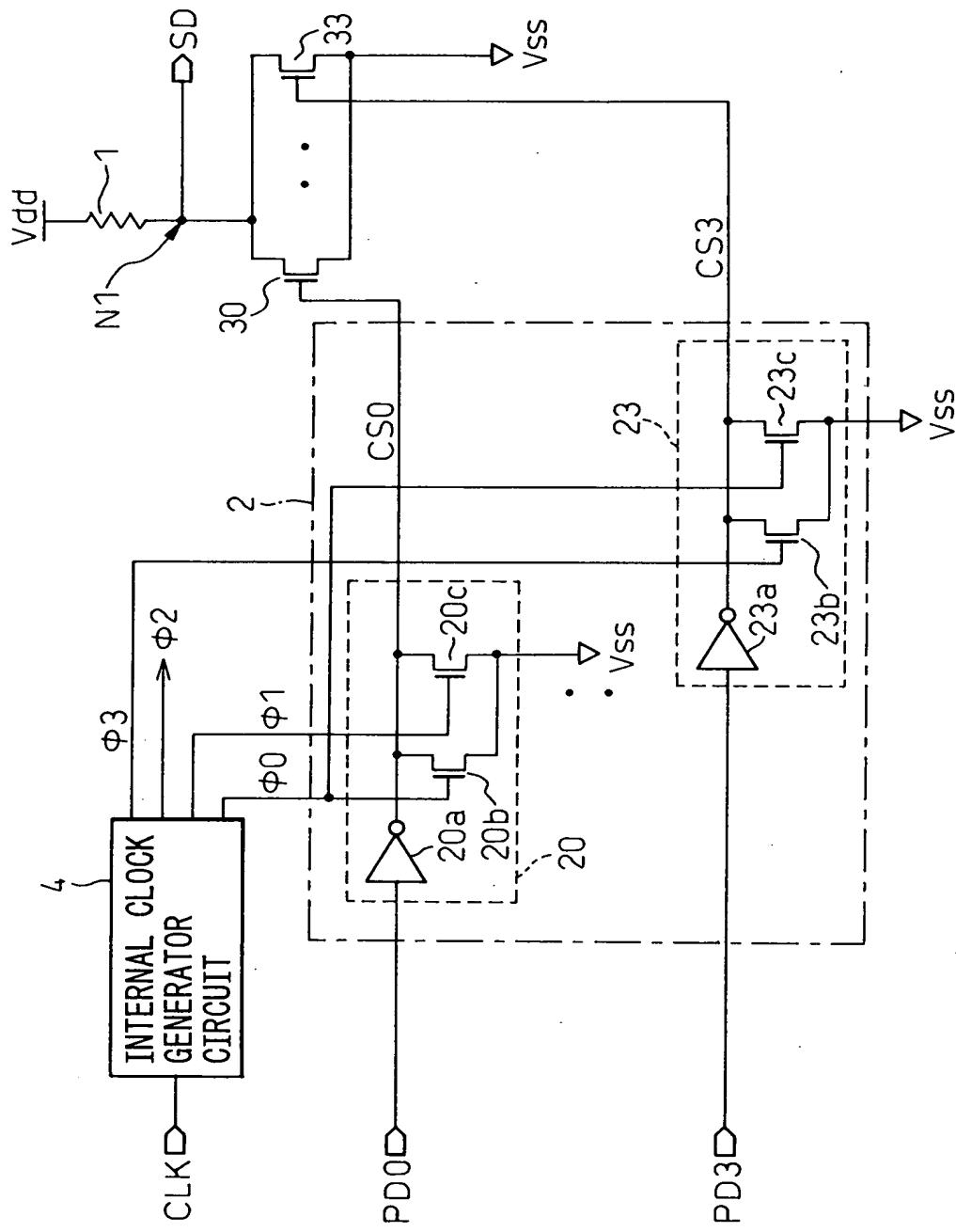


Fig. 4



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Fig.5

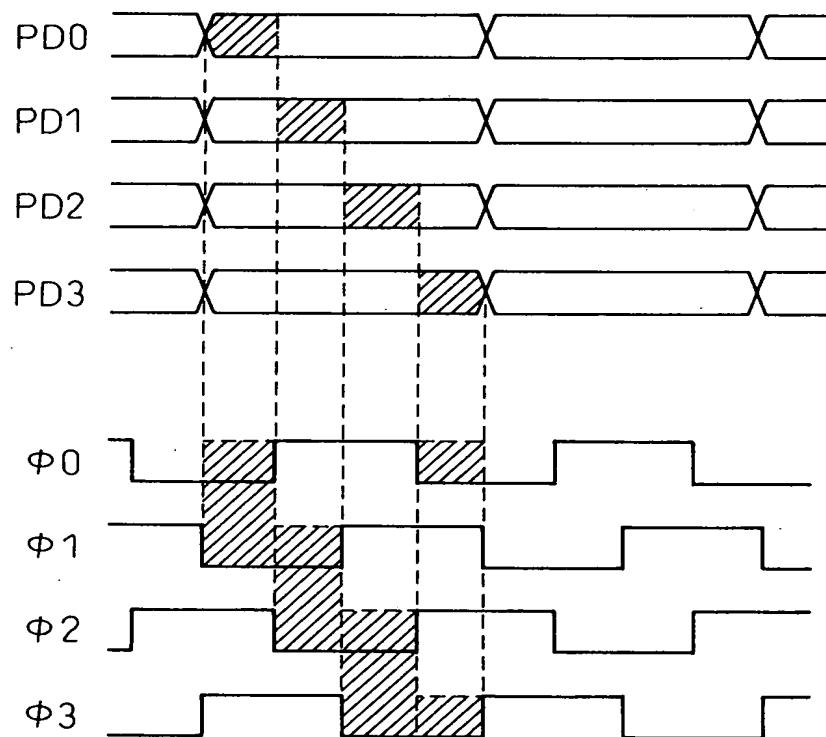


Fig.6

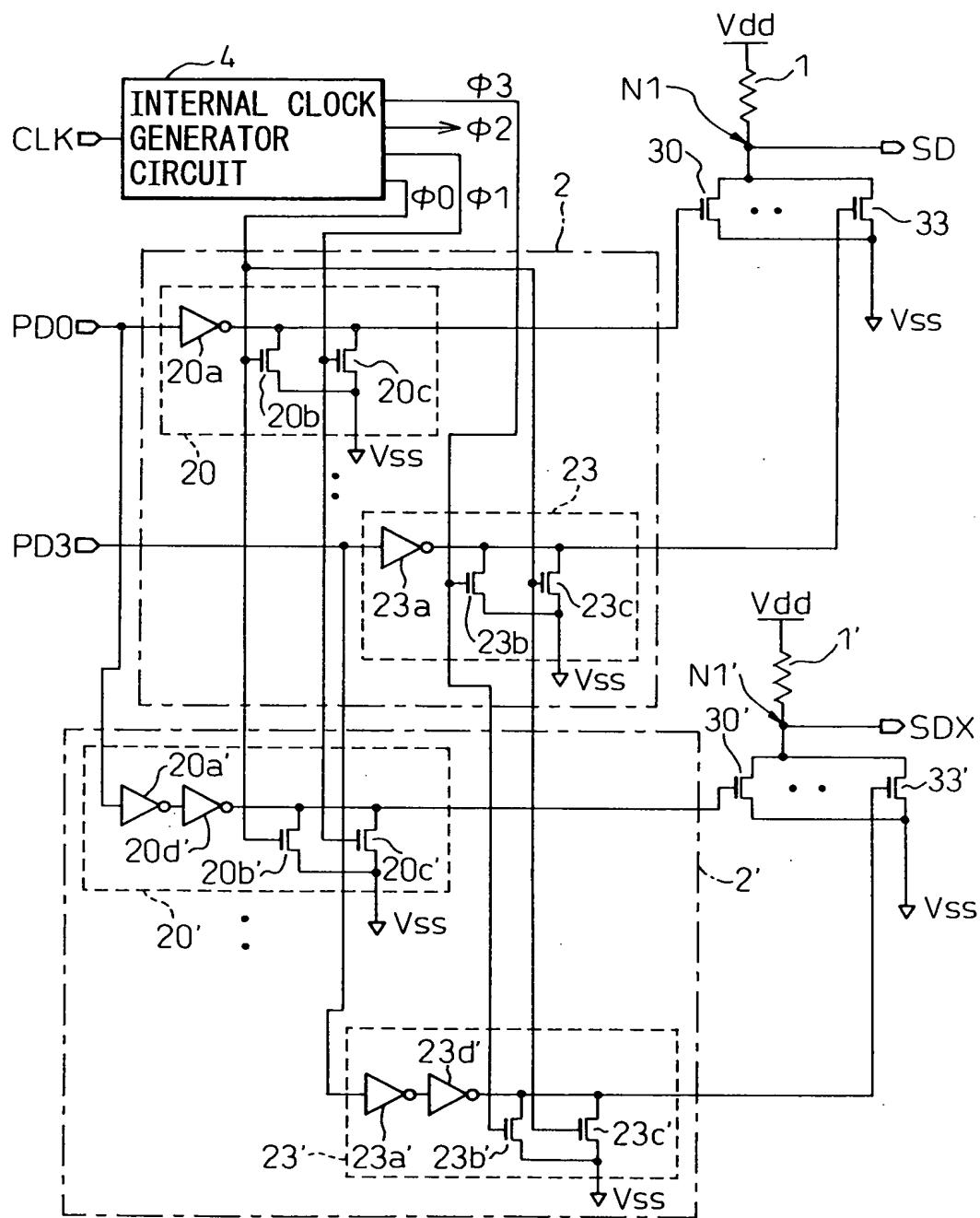


Fig.7

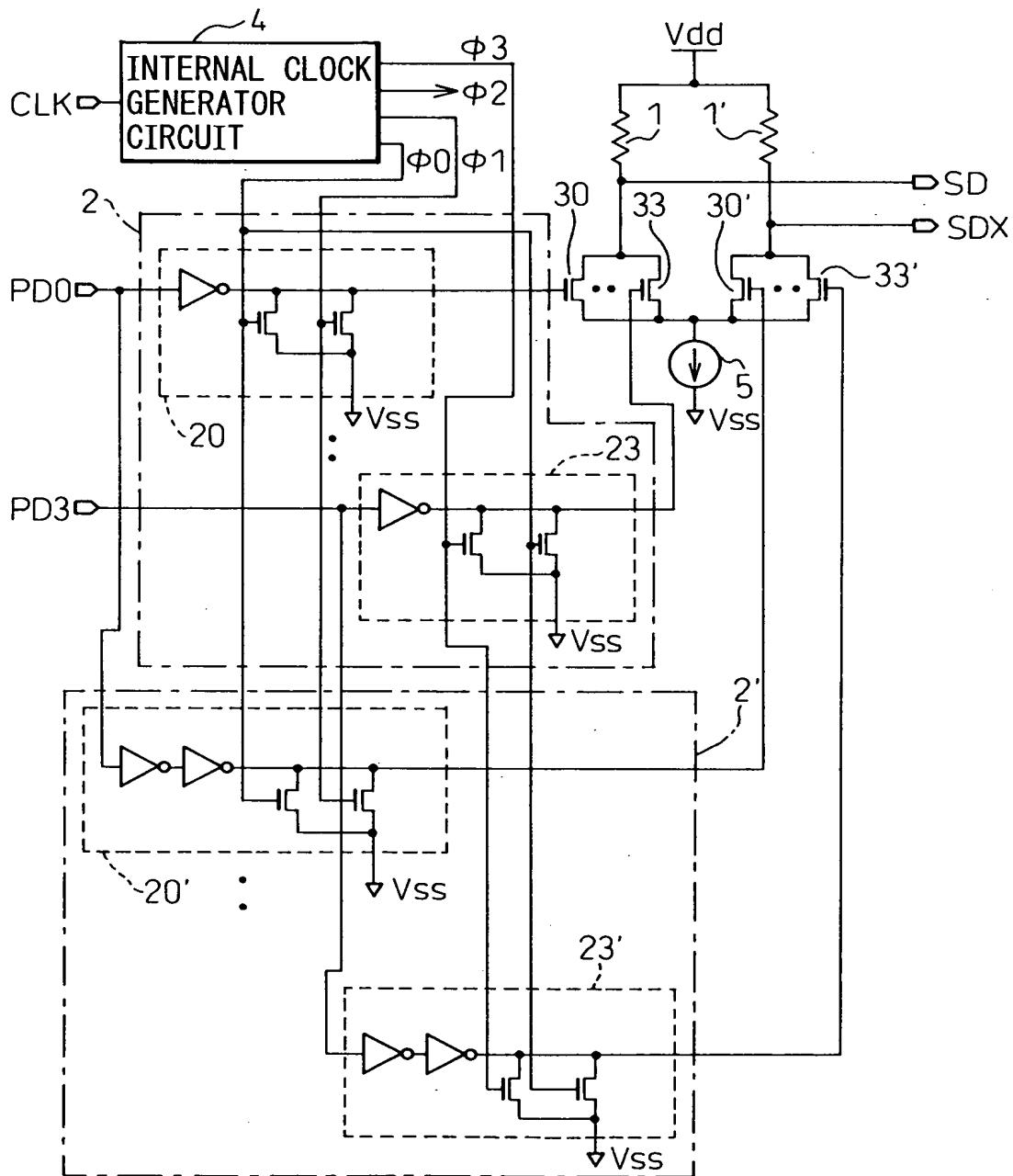


Fig.8

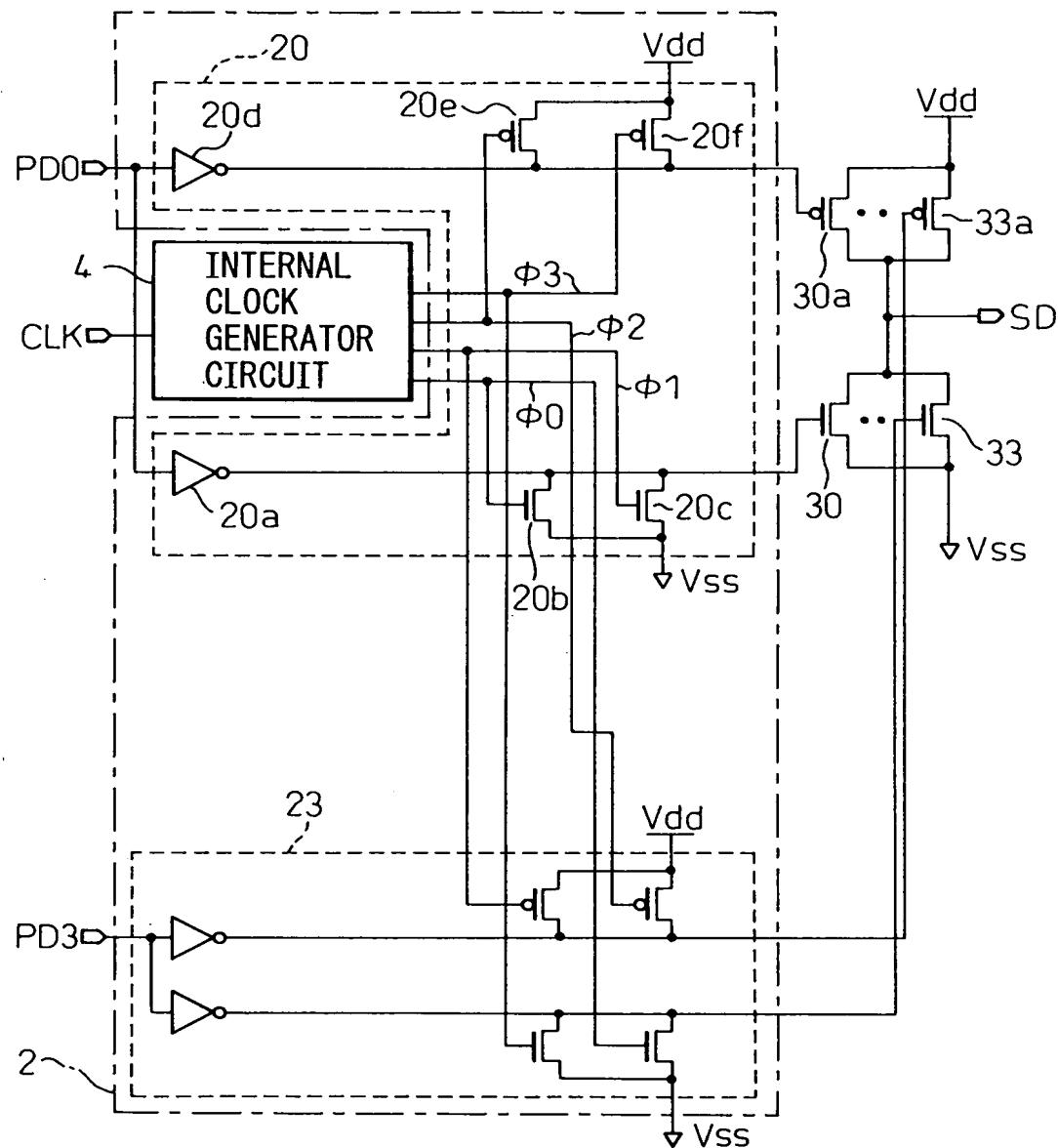
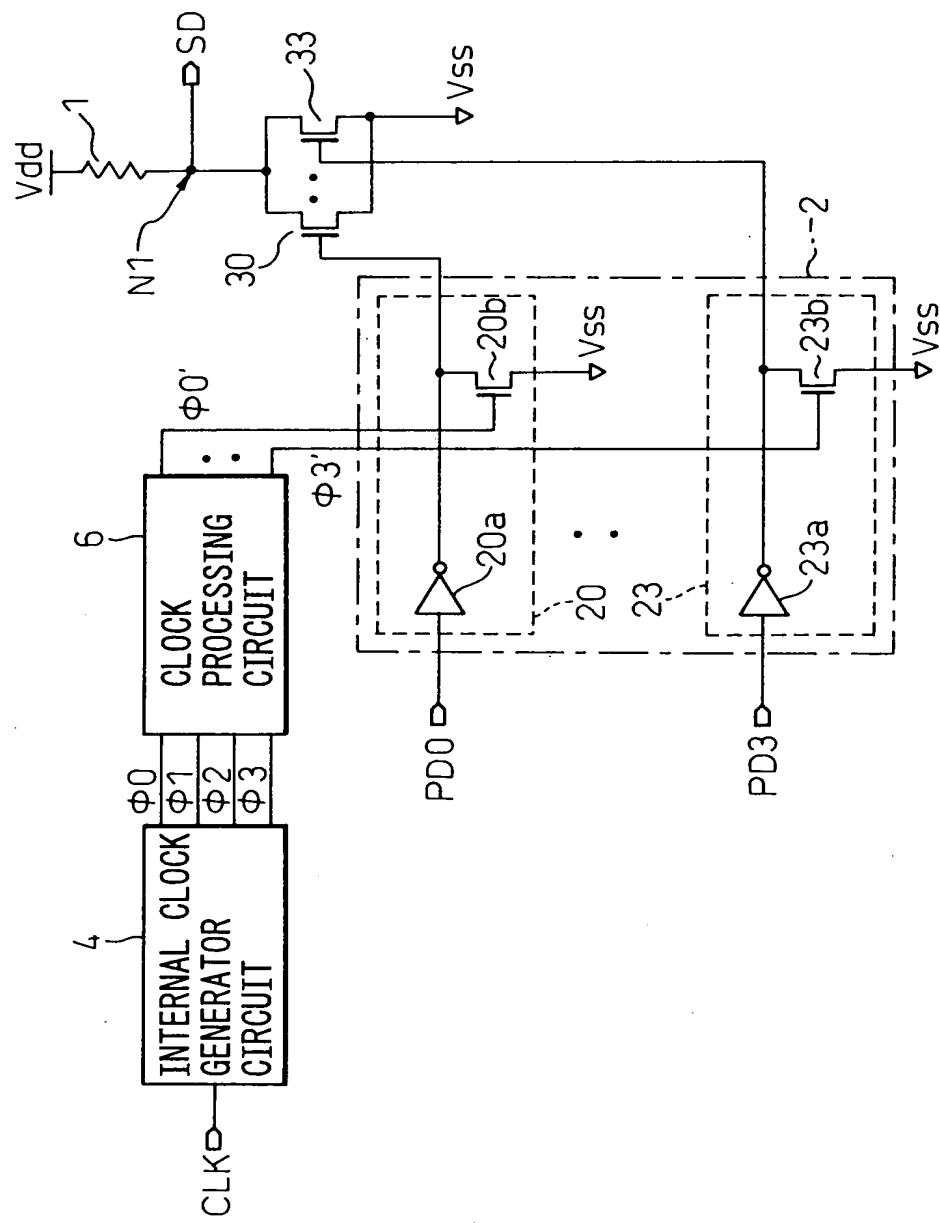


Fig. 9



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Fig.10

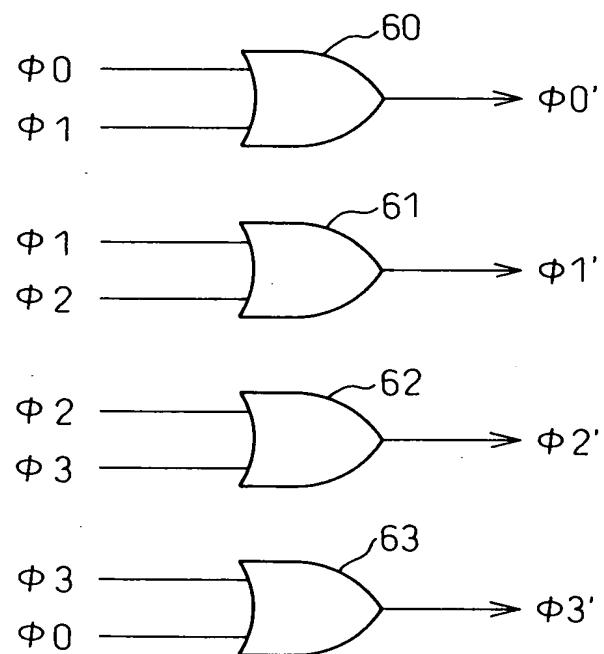
6

Fig.11

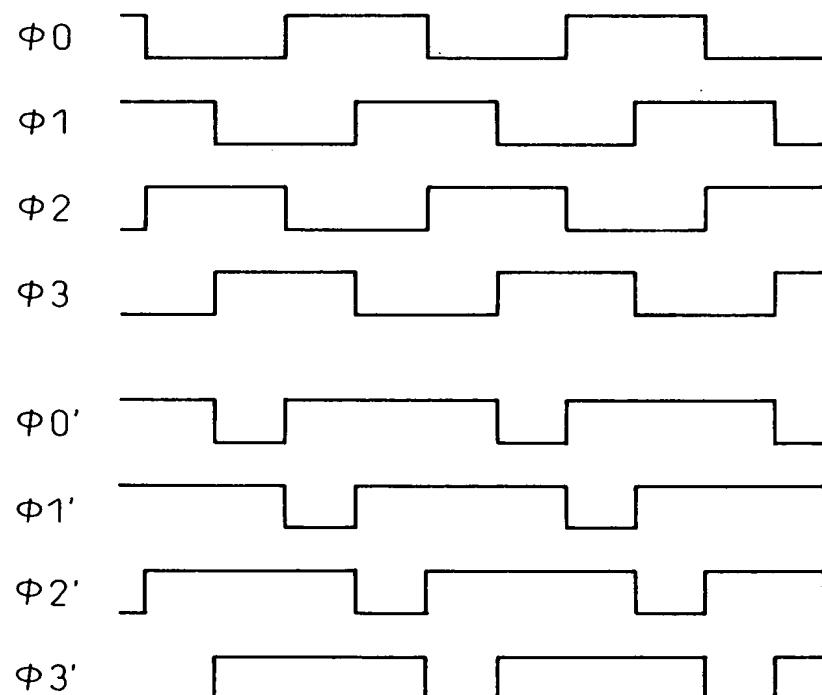


Fig.12

